

Design of the inverter for driving CCFL

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냉음극 방전 램프 구동용 인버터 설계

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Abstract Piezoelectric inverter for driving CCFL is designed in this study. Of them, CCFL is generally used because it has advantages such as small size, high efficiency and good brightness characteristics. the description of the piezoelectric effect is not present here and can be easily found in numerous publications as well as complex equations and formulae. What is the most important to understand is that "they are different" one cannot just change an electromagnetic transformer(EMT) for a piezoelectric one. The simulation program supports the modeled piezoelectric inverter for this paper and the equivalent circuit. The result of the experiment shows more than 90% improvement in terms of the efficiency.

요약 본 논문에서는 냉음극 방전램프 구동용 압전 인버터를 설계하였다. 등가회로의 모델링 방법으로는 진동자의 모델에 많이 이용되고 있는 어드미턴스 퀘적법을 사용하였고, 인버터 설계 시 전자회로 해석프로그램인 PSIM을 사용하여 압전 인버터 설계에 대한 검증을 하였다. 압전기 영향의 기술은 다수의 연구에서 복잡한 방정식과 공식도 쉽게 찾을 수 있다. 특히 압전기 세라믹 EMT가 변화하지 않으며 PT기초 전기소자의 여러 가지 예는 더욱 효과적인 PT의 강점을 이용하는데 도움이 될 것이다. 과적으로, 회로의 단순화와 그에 따른 소형화 그리고 입출력대비 효율이 최대 90%이상으로 우수한 성능을 보였다.

Key Words : CCFL, HCFL, Multilayer Inverter, Transformer, Piezoelectric Actuator

1. Introduction

In comparison with electromagnetic transformers they have some inherent features which should be kept in mind while developing schematics of inverters using CCFL. The brief comparison of two kinds of transformers is in the Table 1. Piezoelectric transformers can be used in the same applications as usual electromagnetic transformers but the most perspective are high output voltage low power

applications and low output voltage, low and middle power (up to 50W) applications. Further on we will speak mostly about high-voltage piezoelectric transformers. In comparison with electromagnetic transformers they have some inherent features which should be kept in mind while developing schematics of inverters using CCFL. The most important for a circuit designer is the resonant frequency response of a PT. The factor, step-up ratio N, resonant frequency and efficiency K of a PT vary with the load resistance.

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2. Main features of PT

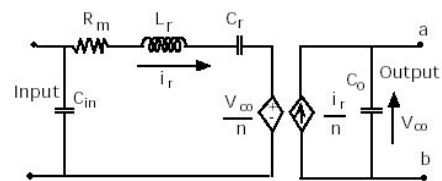
Therefore there are two main problems facing a circuit designer. The first is that the inverter has to operate close to the resonant frequency of the PT even when the load resistance may change. It is necessary to obtain maximum step-up ratio and efficiency. The second is that maximum efficiency for a given PT (the dimensions of the PT are fixed) can be obtained only in a certain range of load resistance. In other words for different loads different PT should be developed.

[Table 1] The comparison of piezoelectric and electromagnetic transformers

	EM transformer	Piezoelectric transformer
Principle	Electromagnetic induction	Piezoelectric effect
Step-up ratio	Wound ratio	Shape ratio(length/thickness, number of layers), load impedance
Frequency response	Flat	Resonant
Output waveform	Input waveform	Sine wave
Input/output impedance	Inductive	Capacitive
Device polarity	Non-polar	Polar
Combustibility	Combustible	Non-combustible
Conversion efficiency	About 85%	Over 95%
Failure form	Burning, open winding	Breaking

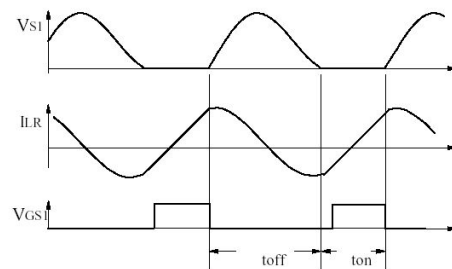
The parameters of the model are usually given by the manufacturer or can be measured as shown in [1]. It should be kept in mind that this is the idealized model which is valid only in the vicinity of the resonant frequency. In fact a PT has several resonant frequencies, so-called harmonics. For every harmonic parameters of the model are different. Usually the first (so-called $\lambda/2$ -mode, where λ is the sound wavelength in ceramic) or the second (λ -mode) harmonics are chosen, because the efficiency and step-up ratio is maximal in this case and decrease quickly with the increasing the number of a harmonic. In a practical

circuit changes in drive level or load characteristics may induce "mode-hopping" that is jumping to other harmonics. So it is necessary to undertake special measures to avoid such jumping. Another important feature is the capacitive input and output impedances of PT. That means that DC current cannot flow through the input or output terminals of PT, though the equivalent circuit model shows it. The more correct model is proposed in [2] and shown at fig 1. The result is that different topology of input driver and output rectifier is used unlike EM transformer.



[Fig. 1] Another equivalent circuit model proposed in[2]

The input driver includes an inductor as a rule. Another reason for this inductor is to match the driver with a PT for maximum efficiency as shown in [3]. The output rectifier which is usually built as a voltage multiplier must contain even number of diodes. Finally it's important that PT has polarity. That means that if the input voltage contains DC component, like in the case of single-ended driver fig 2, it is necessary that positive DC polarity be applied to the "+" electrode of PT. Otherwise the output voltage significantly decreases. Generally it is better to avoid applying DC voltage to PT. In the case of sine input voltage (no DC component) the polarity of PT does not matter.



[Fig. 2] Single-ended driver wave forms (note that PT input voltage V_{S1} contains DC component)

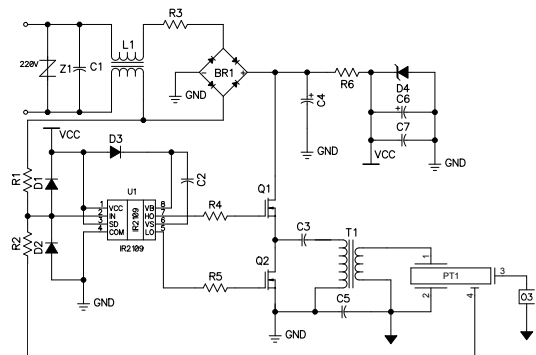
3. Design of piezoelectric inverter for driving LCD backlight

All high-voltage applications of PT may be divided into two groups. The first group has relatively high load resistance - more than tens and hundreds mega ohms. It is typical for image intensifier tubes, ionizers, air cleaners, low power ozone generators, photo multipliers, laser printers and so on. The output voltage can be 1 ~ 6kV and the load current not more than 0,1 ~ 0,5mA. Another group are relatively "high-current" power supplies with output current several milli amps and output voltage 300 ~ 600V. And the typical value of load resistance is 100 ~ 300kOhm. The most popular application of PT nowadays is low profile power supplies for CCFL (cold cathode fluorescent lamp) backlight of LCD displays. Typical specifications of such kind of inverter are in the Table 2 [2]. To achieve the maximum efficiency the piezoelectric transformer must be developed specially for every value of load (a lamp type). The step-up ratio is relatively small in this case (5 ~ 10) and the Q-factor is minimal.

Speaking about the first group of applications the easiest and cheapest way is to use a self-oscillating circuit like shown at Fig. 3. The positive feedback is provided by capacitive antenna. The inductor L1 should be matched to obtain the maximum output voltage.

4. Simulation Study

To improve the load characteristic of a PT-based inverter a push-pull or totem-pole drivers can be used. In this case the input voltage of PT is almost sine which also improves the efficiency. The circuit shown at the fig 3 includes the push-pull drive stage and the pre-amplifier built on the CMOS gates (CD4069). Another feature of this inverter is the varistor Z1 which is used for the stabilizing of the output voltage. As its capacitance is high enough (about 100pF) there is no need in a capacitor connected to the Vout/2 output.



[Fig. 3] Push-pull inverter for air cleaner

The pre-amplifier together with the high-side driver is built on the IR2109 microchip. The transformer T1 solves three problems at once:

[Table 2] Typical specifications of CCFL power supplies[2]

TAMURA PART NO.	INPUT VOLTAGE RANGE	CONTROL VOLTAGE REQUIREMENT	MINIMUM NO LOAD OUTPUT VOLTAGE	OUTPUT CURRENT	MAXIMUM OUTPUT POWER	OPERATING FREQUENCY	DIMMING	H x w x L (mm)	Weight (grams)
HBL-0217	3.0V~5.5V	5V ± 10%	800Vrms	1.1mArms	0.4W	160 KHz	PROVIDED	9.0 x 9.4 x 103.95	4.46
HBL-0210	3.0V~5.5V	5V ± 5%	900Vrms	1.0mArms	0.38W	160 KHz	PROVIDED	8.9 x 9.0 x 59.5	4.14
HBL-0204	3.0V~5.5V	5V ± 5%	900Vrms	2.0mArms	0.65W	160 KHz	PROVIDED	8.9 x 9.0 x 59.5	4.14
HBL-0214	4.5V~5.5V	N/A	1200Vrms	4.0mArms	3.00W	70 KHz	PROVIDED	5.0 x 12.5 x 142.0	11.90
HBL-0215	4.75V~5.25V	N/A	700Vrms	1.5mArms	0.60W	160 KHz	PROVIDED	4.3 x 23.0 x 46.9	3.63
HBL-0218	5.0V~10.0V	N/A	1080Vrms	2.4mArms	1.20W	160 KHz	PROVIDED	6.4 x 9.0 x 56.4	2.61
HBL-0219	8.0V~14.0V	N/A	1300Vrms	3.0mArms	2.20W	100 KHz	PROVIDED	7.4 x 13.6 x 118.0	7.97
HBL-0216	9.0V~15.0V	N/A	1250Vrms	3.7mArms	2.5W	100 KHz	PROVIDED	5.0 x 12.0 x 138.0	8.77

1. It's inductance is necessary for the matching.
2. The step-down voltage ratio helps to apply necessary input voltage to the PT (310V_{p-p} is too high for the PT used).
3. It enables galvanic isolation of the ozone generator from the power line.

5. Conclusion

Proposed inverter is a transformer-less inverter with low. By driven in both sides, effect of stray capacitance can be reduced. Since input voltage of resonant tank is twice as high as that of full-bridge resonant inverter, to drive lamp with low is possible. Only frequency control is possible with symmetric driving method, but both duty control and frequency control are possible with asymmetric driving method, which is applicable to dimming, trim, current control and etc. From experiments with single lamp, the analysis and design are verified. Multi-lamps can be driven by current balancing method using coupled inductor. Proposed inverter is suitable for CCFL driving inverter of operates in the normal mode when the feedback compensates possible changes of output voltage or current by means of changing the operating frequency.

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<Research Interests>

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<Research Interests>

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